

ABSTRACT

A bus arbitration apparatus includes a storage, a priority order determiner, and an arbitrator. The storage stores a plurality of selection signals for specifying a priority order against a number N of requests. The priority order determiner causes the storage to output one of the plurality of selection signals in a predetermined sequence in response to a demand for arbitration. The arbitrator performs an arbitration operation based on the priority order against the number N of requests specified by one of the plurality of selection signals which is output from the storage.